

Sheet 1 of 1

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501299.01	APPLICATION NO. Not Yet Assigned 10/644,610
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		APPLICANT(S) Tae H. Kim	FILING DATE Concurrently herewith
		GROUP ART UNIT Not Yet Assigned	2818

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
AK						
AL						
AM						

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

 <small>AN</small>	Kiriata, T. et al., "A 113mm ² 600Mb/s/pin 512Mb DDR2 SDRAM with Vertically-Folded Bitline Architecture", IEEE International Solid State Circuits Conference, February 2001, pp. 382-383 and 468.
 <small>AO</small>	Yoon, H. et al., "A 4Gb DDR SDRAM with Gain-Controlled Pre-Sensing and Reference Bitline Calibration Schemes in the Twisted Open Bitline Architecture", IEEE International Solid-State Circuits Conference, February 2001, pp. 378-379 and 467.

EXAMINER <div style="font-size: 2em; font-family: cursive;">H. HO</div>	DATE CONSIDERED <div style="font-size: 1.5em; font-family: cursive;">9/8/04</div>
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* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).